

**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

Docket No.: 907A.0141.U1(US)

Serial No.: 10/820,347

Applicant(s): Hall et al.

Filing Date: 4/7/2004

Group: 2631

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
	US-				
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FOREIGN PATENT DOCUMENTS

Examiner Initials	Document Number (Country Code-Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name Of Patentee of Applicant	Translation? Yes/No/n/a
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OTHER DOCUMENTS (Author (Capitalize), Title, Date, Pages, Etc., if known)

KT	"Design and Analysis of Turbo Codes on Rayleigh Fading Channels", Eric K. Hall et al., IEEE Journal On Selected Areas In Communicatins, Vol. 16, No. 2, Februry 1998, pp. 160-174

Examiner's Signature: /Kabir Timory/

Date Considered: 05/03/2007

Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered.
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**INFORMATION DISCLOSURE
CITATION FORM FOR
PATENT APPLICATION
(FORM PTO-1449)
(Substitute)**

Docket No.: 907A.0141.U1(US)

Serial No.: 60/820,377

Applicant(s): Hall et al.

Filing Date: herewith

Group 2609

U.S. PATENT DOCUMENTS

Examiner Initials	Document Number (Number-Kind Code)	Publication Date (MM-DD-YYYY)	Name of Patentee or Applicant	Class	Sub-class
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KT	"Turbo Decoding with Erasures for High-Speed Transmission in the Presence of Impulse Noise", Lliang Zhang et al., IEEE 2002, pp. 20-1 -- 20-6
KT	"A Serial-Parallel Concatenated System: Construction and Iterative Decoding with Erasures", Jan Bajcsy et al., IEEE 1999, pp. 1192-1196
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KT	"A High Speed Successive Erasure BCH ¹ Decoder Architecture", Thomas Buerner, Institute for Computer Aided Circuit Design, 2003, 6 pages

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